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August 21, 2007

**FREESCALE SEMICONDUCTOR, INC.**  
**LAW DEPARTMENT**  
**7700 WEST PARMER LANE MD:TX32/PL02**  
**AUSTIN TX 78729**

Patent No. : 7,144,825 B2  
Inventor(s) : Olubunmi O. Adetutu, et al.  
Issued : December 5, 2006  
For **MULTI-LAYER DIELECTRIC  
CONTAINING DIFFUSION BARRIER  
MATERIAL**  
Doc. No. **SC12752TP**

To Whom It May Concern:

The Certificate of Correction issued on August 21, 2007, issued in error, in that error(s) was made in identifying the patent number and/or keying text/corrections, i.e.:

The issue date in the heading of cofc has been incorrectly entered in the heading of the issued cofc. A superseding cofc will be issued to correct the patent issue date to read --December 5, 2006--.

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Therefore, a certificate of correction will be issued to correct (supersede) the Certificate of Correction containing error(s), made during preparation of the Certificate of Correction, as noted above.

No further response is required, from applicants (attorney). However, errors discovered by attorney, other than as noted and described above, should be noted on *a copy* of the Certificate of Correction that was issued in error, accompanied by a signed transmittal letter and submitted directed to this Branch.

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cbn